

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Patent Application of:	
<b>Seong-Min Choe</b>	Examiner: Daniel D. Chang
Serial No. 10/737,069	Art Unit: 2819
Filed: December 15, 2003	Confirmation No. 5251
<b>ON-DRAM TERMINATION RESISTANCE CONTROL CIRCUIT AND METHOD THEREOF</b>	

Mail Stop Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Dear Commissioner:

In response to the Office Action mailed December 12, 2006, regarding the above-referenced application, Applicant respectfully requests entry of the amendment set forth below and consideration of the remarks that follow.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 8 of this paper.